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CONFIRMATION NO. 2864

SERIAL NUMBER 10/616,981	FILING DATE 07/11/2003 RULE	CLASS 257	GROUP ART UNIT 2815	ATTORNEY DOCKET NO. L8462.01108A
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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 09/972,884 10/10/2001 PAT 6,693,358

** FOREIGN APPLICATIONS *****

JAPAN 2000-322908 10/23/2000
 JAPAN 2000-331833 10/31/2000
 JAPAN 2000-350976 11/17/2000
 JAPAN 2000-350977 11/17/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 10/16/2003

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	DRAWING 70	14	15

ADDRESS

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 Suite 850
 1615 L Street, N.W.
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TITLE

Semiconductor chip, wiring board and manufacturing process thereof as well as semiconductor device

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of
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